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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Theodore W. Houston

Serial No.: N/A

Filed: Herewith

For: A MULTI-GATE ONE-TRANSISTOR DYNAMIC RANDOM
ACCESS MEMORY

Group No.: N/A

Examiner: N/A

Commissioner for Patents
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Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
6,396,108 B1	Krivokapic et al.	May 28, 2002
6,307,230 B1	Chatterjee et al.	October 23, 2001
6,225,175 B1	Houston	May 1, 2001
<u>Foreign Patent No.</u>	<u>Country</u>	<u>Date</u>
WO 02/103703 A2	WO	December 27, 2002

References:

Kazumi Inoh, Tomoaki SHino, Hiroaki Yamada, Hirooomi Nakajima, Yoshihiro Minami, Takashi Yamada, Takashi Ohsawa, Tomoki Higashi, Katsuyuki Fujita, Tamio Ikehashi, Takeshi Kajiyama, Yoshiaki Fukuzumi, Takeshi Hamamoto and Hidemi Ishiuchi; "FBC (FLOATING BODY CELL) FOR EMBEDDED DRAM ON SOI"; 2003 Symposium on VLSI Technology Digest of Technical Papers; pgs 1-2.

Takashi Ohsawa, Tomoki Higashi, Katsuyuki Fujita, Tamio Ikehashi, Takeshi Kajiyama, Yoshiaki Fukuzumi, Tomoaki Shino, Hiroaki Yamada, Hirooomi Nakajima, Yoshihiro Minami, takashi Yamada, Kazumi Inoh, Takeshi Hamamoto; "A MEMORY USING ONE-TRANSISTOR GAIN CELL ON SOI (FBC) WITH PERFORMANCE SUITABLE FOR EMBEDDED DRAM'S"; 2003 Symposium on VLSI Circuits Digest of Technical Papers; Pgs1-4.


B. Doyle, B. BOyanov, S. Datta, M. Doczy, S. Harland, B. Jin J. Kavalieros, T. Linton, R. Rios & R. Chau; "TRI-GATE FULLY-DEPLETED CMOS TRANSISTORS: FABRICATION, DESIGN AND LAYOUT"; 2003 Symposium on VLSI Technology Digest of Technical Papers; Pgs.1-2.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement. The Commissioner is hereby authorized to charge any fees which may be required, or

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Respectfully submitted,

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	N/A
Filing Date	Herewith
First Named Inventor	Theodore Houston, et al.
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	TI 35657

Sheet	1	of	2
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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			
		WO 02/103703 A2	12/27/2002	Innovative Silicon SA		

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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				Application Number	N/A
				Filing Date	Herewith
				First Named Inventor	Theodore Houston, et al.
				Art Unit	N/A
				Examiner Name	N/A
Sheet	2	of	2	Attorney Docket Number	TI 35657

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Kazumi Inoh, Tomoaki SHino, Hiroaki Yamada, Hiroomi Nakajima, Yoshihiro Minami, Takashi Yamada, Takashi Ohsawa, Tomoki Higashi, Katsuyuki Fujita, Tamio Ikehashi, Takeshi Kajiyama, Yoshiaki Fukuzumi, Takeshi Hamamoto and Hidemi Ishiuchi; "FBC (FLOATING BODY CELL) FOR EMBEDDED DRAM ON SOI"; 2003 Symposium on VLSI Technology Digest of Technical Papers; pgs 1-2.	
		Takashi Ohsawa, Tomoki Higashi, Katsuyuki Fujita, Tamio Ikehashi, Takeshi Kajiyama, Yoshiaki Fukuzumi, Tomoaki Shino, Hiroaki Yamada, Hiroomi Nakajima, Yoshihiro Minami, takashi Yamada, Kazumi Inoh, Takeshi Hamamoto; "A MEMORY USING ONE-TRANSISTOR GAIN CELL ON SOI (FBC) WITH PERFORMANCE SUITABLE FOR EMBEDDED DRAM'S"; 2003 Symposium on VLSI Circuits Digest of Technical Papers; Pgs1-4.	
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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